

AMENDMENTS TO THE SPECIFICATION

Please add the following new paragraphs to the Specification.

Please insert the following two paragraphs following paragraph [0011] in the Brief

Description of the Drawings section on page 4:

Fig. 2 illustrates a view of the channel path from $x(n)$ to $y(n)$ according to an embodiment of the present invention.

Fig. 3 illustrates a block diagram of the cascade of I/Q compensator and the upconverter according to an embodiment of the present invention.